

# Download Three Dimensional Integrated Circuit Layout

In microelectronics, a three-dimensional integrated circuit (3D IC) is an integrated circuit manufactured by stacking silicon wafers or dies and interconnecting them vertically using, for instance, through-silicon vias (TSVs) or Cu-Cu connections, so that they behave as a single device to achieve performance improvements at reduced power and ...Layout designs (topographies) of integrated circuits are a field in the protection of intellectual property. In United States intellectual property law, a "mask work" is a two or three-dimensional layout or topography of an integrated circuit (IC or "chip"), i.e. the arrangement on a chip of semiconductor devices such as transistors and passive ...As a matter of course macros or scripts are possible with the LayoutEditor. It can be used for different applications. So, with the LayoutEditor it is possible to record macros from the user interface like some office programs can do.Integrated Circuits. As all these arrays of components, microscopic circuits and semiconductor wafer material base are integrated together to form a single chip, hence, it is called as integrated circuit or integrated chip or microchip.